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Docket No.: 122.1466

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yuji SANO, et al.

Serial No. 09/933,166

Group Art Unit: 2821

Confirmation No. 6450

Filed: August 21, 2001

Examiner: Lee Wilson

For: CAPACITIVE-LOAD DRIVING CIRCUIT CAPABLE OF PROPERLY HANDLING  
TEMPERATURE RISE AND PLASMA DISPLAY APPARATUS USING THE SAME

**AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

This is in response to the Office Action mailed January 8, 2003, and having a period for response set to expire on February 8, 2003.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.

**IN THE SPECIFICATION:**

Please REPLACE the paragraph beginning at page 10, line 15, as follows:

*A1*

The following description deals primarily with the address driving circuit (address drive IC) of the plasma display apparatus, but it will be recognized that the capacitive-load driving circuit of the invention can be applied not only for the address driving circuit of the plasma display apparatus, but also for other circuits for driving capacitive loads (e.g., discharge cells), such as the X driving circuit and the Y driving circuit; furthermore, the circuit technique of the invention can be applied extensively to circuits for driving various capacitive loads other than those in a plasma display apparatus, for example, to circuits for driving logic gates formed from MOS transistors (the gate of each transistor to be driven can be considered a capacitor, with which a capacitor or the like, parasitic on an interconnection, etc., is combined to form a capacitive load).

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